

<b>Notice of References Cited</b>		Application/Control No. 10/621,067	Applicant(s)/Patent Under Reexamination FARKAS ET AL.	
		Examiner KENNETH TANG	Art Unit 2195	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-			
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	D	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

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#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Kumar et al. "Processor Power Reduction Via Single-ISA Heterogeneous Multi-Core Architectures", IEEE Computer Architecture Letters, Vol. 2, April 2003, pages 1-4
	V	Kumar et al. "Processor Power Reduction Via Single-ISA Heterogeneous Multi-Core Architectures", IEEE Computer Architecture Letters, Volume 1, Issue 1, January 2002, Abstract.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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